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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/009,858	12/22/2001	Bernhard Raaf	112740-344	6325
29177	7590	08/20/2007	EXAMINER	
BELL, BOYD & LLOYD, LLP			SAMS, MATTHEW C	
P.O. BOX 1135			ART UNIT	PAPER NUMBER
CHICAGO, IL 60690			2617	
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			08/20/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

**Advisory Action
Before the Filing of an Appeal Brief**

Application No.

10/009,858

Applicant(s)

RAAF, BERNHARD

Examiner

Matthew C. Sams

Art Unit

2617

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 20 July 2007 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

a) The period for reply expires 3 months from the mailing date of the final rejection.
 b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because

- (a) They raise new issues that would require further consideration and/or search (see NOTE below);
- (b) They raise the issue of new matter (see NOTE below);
- (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5. Applicant's reply has overcome the following rejection(s): _____.

6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____.

Claim(s) objected to: _____.

Claim(s) rejected: _____.

Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.

12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____.

13. Other: _____.

LESTER G. KINCAID
SUPERVISORY PRIMARY EXAMINER

Continuation of 11. does NOT place the application in condition for allowance because: With respect to the applicant's argument regarding "embedding the power control information in a timeslot structure together with further data to be transmitted in the same timeslot" (Page 8), the examiner disagrees. Uesugi clearly teaches in Fig. 16A & 16B a time slot that includes power control information (TPC) and further data (Data of Specified Pattern, D0, D1...D6).

With respect to the applicant's argument regarding "coding" (Page 8), the examiner disagrees. Uesugi clearly teaches the transmission of symbols and bits (Col. 11 line 15 & 51), which obviously requires a "coding" process to be represented as symbols and bits in order to be transmitted.

With respect to the "addition of redundancy" (Page 8 & 9), it is not clearly claimed what is redundant, therefore the redundancy shown in Fig. 16B and disclosed in Col. 19 lines 46-52 meets the limitation.

With respect to the "further data to be transmitted in the same time slot to form a common data word", Uesugi clearly teaches further data in Fig. 16B [D0, D1 & D6], transmitted as a common data word. (First Slot).

With respect to the "one bit value in the data word depending on the power control information and on the further data" (Page 8 & Page 9), adding a parity bit or error correction control bit is well known in the art. The entire time slot is XOR'd, with the additional bit dependent upon the values of the coded time slot. Hogan clearly teaches the use of XOR logic for performing error coding correction with the addition of an error correction bit. (Page 1 [0012-0013]) Transmitting an error correction bit along with the transmission clearly minimizes repetitive transmissions because the receiver has ability to recover the original information from the noise of the transmission.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., number of output bits are formed dependent upon the number of input bits" (Page 8)) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

With respect to the applicant's arguments regarding "encrypted data" (Page 9), an encryption scheme is as simple as representing a 1 as a 1 and a 0 as a -1. (i.e. coding for digital communication) In other words, Uesugi obviously already handles "encrypted data", the examiner is just modifying the technique to include an XOR function and adding a bit to the transmission in order to recover the original information.